

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): David F. Tobias et al.

Assignee: Advanced Micro Devices, Inc.

Title: SYSTEM AND METHOD FOR CONTROLLING AN INTEGRATED
CIRCUIT TO ENTER A PREDETERMINED PERFORMANCE STATE
BY SKIPPING ALL, INTERMEDIATE STATES BASED ON THE
DETERMINED UTILIZATION OF THE INTEGRATED CIRCUIT

Patent No.: 7,254,721

Issued: August 7, 2007

Atty. Docket No.: 1001-0179

Conf. No.: 9539

September 7, 2007

ATTN: Certificate of Correction Branch
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT –
PTO MISTAKE (37 C.F.R. § 1.322(a))**

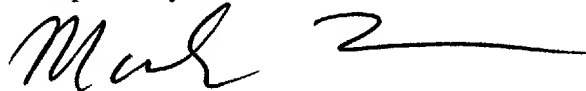
Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:
Examiner's Amendment in Notice of Allowance mailed May 9, 2007, Detailed Action,
page 2, lines 2-5.
3. Please send the Certificate to:

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Respectfully submitted,



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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO: 7,254,721
APPLICATION NO.: 09/876,291
ISSUE DATE: August 7, 2007
INVENTOR(S): David F. Tobias et al.

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover Page, Title (54), lines 2 and 7 and Col. 1, lines 2 and 7, please replace the word "INTERGRATED" with --INTEGRATED--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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